

PATENT  
SK01002DIV (01CXT00018W)

**IN THE CLAIMS**

Cancel claims 1-20 without prejudice.

21(Amended). A circuit, comprising:

a linear amplifier bias circuit comprising first means for adjusting for process variations and environmental variations; and

a non-linear amplifier bias circuit comprising second means for adjusting for process variations and environmental variations, wherein the second means for adjusting for process variations and environmental variations is a function of the first means for adjusting for process variations and environmental variations, where the first means is a current mirror.

22(Original). The circuit of claim 21, further comprising:

a linear amplifier coupled to the linear amplifier bias circuit, wherein  
the linear amplifier bias circuit comprises a reference device that is disposed in relation to the linear amplifier so as to comprise the first means for adjusting for process variations and environmental variations.

23(Deleted). ~~The circuit of claim 21, wherein the first means for adjusting for process variations and environmental variations comprises a current mirror.~~

24(Original). The circuit of claim 21, wherein the first means for adjusting for process variations and environmental variations comprises means for producing a first voltage and the second means for adjusting for process variations and environmental variations includes means  
for producing a second voltage that is a function of the first voltage.

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25(Original). The circuit of claim 21, wherein the linear amplifier bias circuit comprises a voltage follower coupled to a reference device, the circuit further comprising:

a carrier amplifier coupled to the voltage follower.

26(Original). The circuit of claim 25, wherein the non-linear amplifier bias circuit comprises a voltage buffer coupled to a scale/level shift circuit, the circuit further comprising:

a non-linear amplifier coupled to the voltage buffer, wherein the scale/level shift circuit is coupled to the voltage follower.

27(Original). The circuit of claim 26, wherein the linear amplifier is configured to function as a carrier amplifier and the non-linear amplifier is configured to function as a peaking amplifier in a Doherty amplifier.

28(Original). The circuit of claim 27, wherein the linear amplifier bias circuit is a class AB amplifier bias circuit and wherein the non-linear amplifier bias circuit is a class C amplifier bias circuit.

29(Amended). A method, comprising:

adjusting for process variations and environmental variations with a first means in a linear amplifier bias circuit; and

adjusting for process variations and environmental variations with a second means in a non-linear amplifier bias circuit, wherein the second means for adjusting for process variations

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and environmental variations is a function of the first means for adjusting for process variations and environmental variations, where the first means comprises a current mirror.

30(Added). The method of claim 29, further comprising:

biasing a with a linear amplifier bias circuit that is coupled to the liner amplifier, wherein the linear amplifier bias circuit comprises a reference device that is disposed in relation to the linear amplifier so as to comprise the first means from adjusting for process variations and environmental variations.

31(Deleted). ~~The method of claim 29, wherein the first means for adjusting for process variations and environmental variations comprises a current mirror.~~

32(Added). The method of claim 29, wherein the first means for adjusting for process variations and environmental variations comprises means for producing a first voltage and the second means for adjusting for process variations and environmental variations includes means for producing a second voltage that is a function of the first voltage.